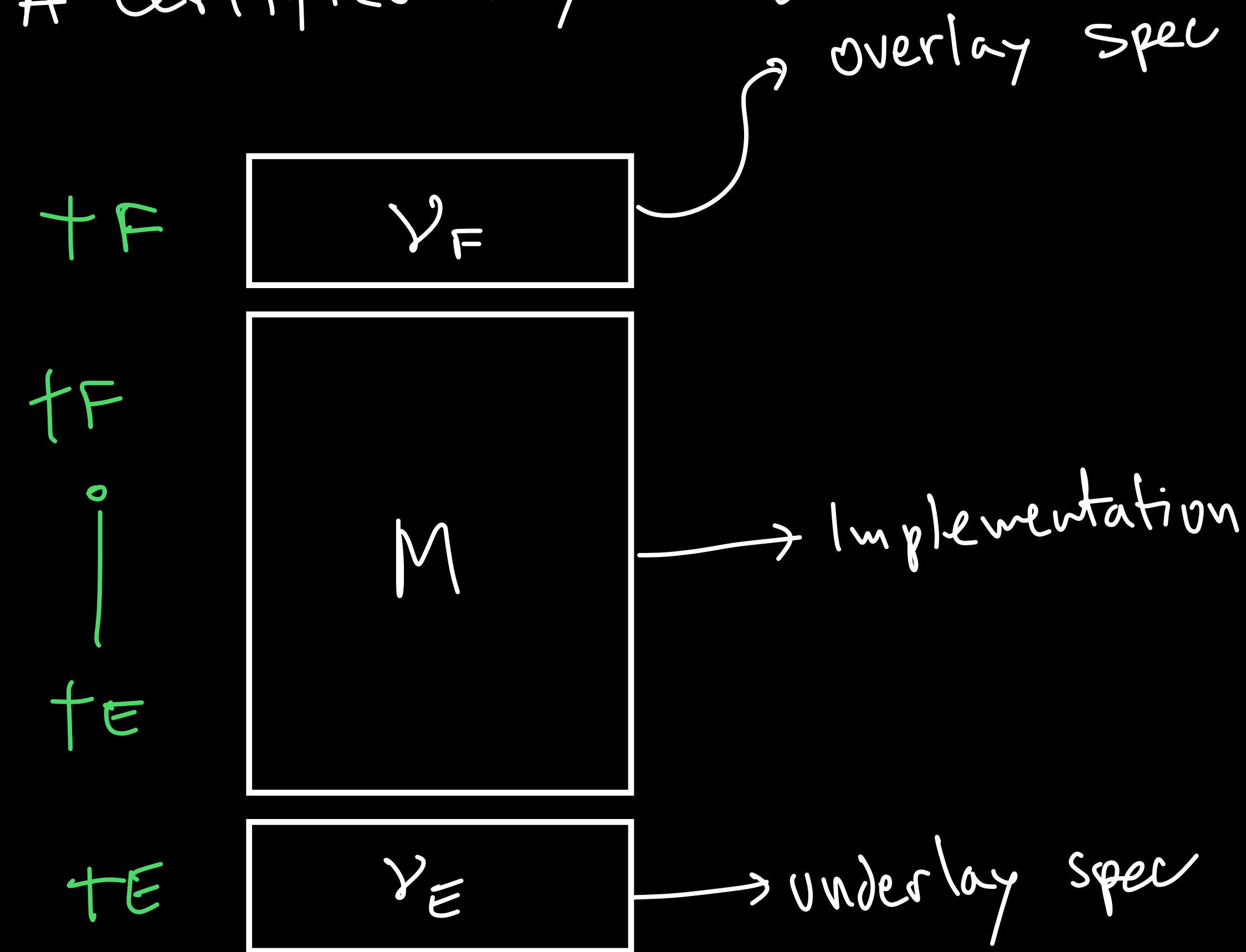


Linearizability and Compositional CCAL

Arthur Oliveira Vale - Feb 22nd 2024

Object-Based CAL Review

A certified layer is:



A spec (strategy) is a set of "well-formed" traces s.t.

- non-empty
- prefix-closed
- receptive
- deterministic

alternating
+
type-dependent

Implementations are regular:

$$M: TE \rightarrow F$$

$$\hat{M}: TE \rightarrow TF$$

Certified when refinement condition holds:

$$V_E; \hat{M} \supseteq V_F$$

What should a concurrent implementation look like?

Simple idea: several "threads" running sequential code in parallel

```
Import X: Var[Nat]
Nat get() {
  v ← x.rd();
  ret v
}
unit inc() {
  v ← x.rd();
  _ ← x.wrt(v+1);
  ret ok
}
```

• • •

```
Import X: Var[Nat]
Nat get() {
  v ← x.rd();
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  v ← x.rd();
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```

What should a concurrent implementation look like?

Simple idea: several "threads" running sequential code in parallel

γ \leadsto set of agent names

$\alpha \in \gamma$ \leadsto agent α
 \leadsto implementation that α runs

$M[\alpha]: TE \rightarrow TF$

\leadsto concurrent implementation

$M[\gamma] = (M[\alpha])_{\alpha \in \gamma}$

Vertical composition:

$$(N[\alpha])_{\alpha \in \gamma} \circ (M[\alpha])_{\alpha \in \gamma} \stackrel{\Delta}{=} (N[\alpha] \circ M[\alpha])_{\alpha \in \gamma} \\ = (\hat{M}[\alpha]; N[\alpha])_{\alpha \in \gamma}$$

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What does it mean to be "well-formed"
Simple idea: interleaving of well-formed sequential traces of each thread

T_{tE} = "well-formed sequential tE traces"

$\gamma: T_{tE} = \parallel_{\text{der}} \alpha: T_{tE}$

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T_{tE} = "well-formed sequential tE traces"

$$\gamma: T_{tE} = \parallel_{\text{der}} \alpha: T_{tE}$$

Example: $\gamma = \{0, 1\}$ $E = \text{Counter}$

1: inc
1: ok
1: get

||

2: get
2: 0

=>

1: inc 1: ok 1: set 2: get 2: 0
1: inc 1: ok 2: set 1: get 2: 0
1: inc 2: set 1: ok 1: get 2: 0
2: set 1: inc 1: ok 1: get 2: 0
2: set 1: inc 1: ok 2: 0 1: get
⋮
2: set 2: 0 1: inc 1: ok 1: get

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~~alternating~~
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Simple idea: interleaving of well-formed sequential traces of each thread

T_{tE} = "well-formed sequential tE traces"

$$\gamma: T_{tE} = \parallel_{\alpha \in \Gamma} \alpha: T_{tE}$$

Example: $\Gamma = \{1, 2\}$ $E = \text{Counter}$

1:inc 1:ok 1:set 2:get 2:0
 1:inc 1:ok 2:set 1:get 2:0
 1:inc 2:set 1:ok 1:get 2:0
 2:set 1:inc 1:ok 1:get 2:0
 2:set 1:inc 1:ok 2:0 1:get
 ...
 2:set 2:0 1:inc 1:ok 1:get

1:inc
1:ok
1:set

||

2:get
2:0

=

NOT Alternating

What is a concurrent spec?

- sequential counter
get

A spec (strategy) is a set σ
of "well-formed" traces s.t.

- non-empty
- prefix-closed
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- deterministic

+ type-dependent
+ interleavings of
well-formed
sequential
traces

What is a concurrent spec?

- sequential counter
get $\rightarrow 0$

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• Sequential counter
get $\rightarrow 0$

• Concurrent counter
1: get

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• Sequential counter
get $\rightarrow 0$

• Concurrent counter
1: get 1:0

What is a concurrent spec?

A spec (strategy) is a set σ of "well-formed" traces s.t.

- non-empty
- prefix-closed
- receptive
- ~~deterministic~~

+ type-dependent
+ interleavings of
well-formed
sequential
traces

• Sequential counter
get $\rightarrow 0$

• Concurrent counter

1: get 1: 0
1: get 2: inc 2: OK 1: 1
1: get 2: inc 2: OK 2: inc 2: OK 1: 2
:
1: get (2: inc 2: OK)* 1: n

NOT Deterministic

What is a concurrent spec?

A spec (strategy) is a set σ of "well-formed" traces s.t.

- non-empty
- prefix-closed
- receptive

+ type-dependent
+ interleavings of
well-formed
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traces

Receptive?

What if I want to describe
an atomic counter?

What is a concurrent spec?

A spec (strategy) is a set σ of "well-formed" traces s.t.

- non-empty
- prefix-closed
- receptive

+ type-dependent
+ interleavings of well-formed sequential traces

Receptive?

What if I want to describe an atomic counter?

1: inc

1: inc 1: OK

1: inc 1: OK 2: get

1: inc 1: OK 2: get 2: 1

⋮

"alternating but threaded"

What is a concurrent spec?

A spec (strategy) is a set σ of "well-formed" traces s.t.

- non-empty
- prefix-closed
- ~~receptive~~

+ type-dependent
+ interleavings of
well-formed
sequential
traces

Receptive?

What if I want to describe
an atomic counter?

1: inc 2: get

1: inc 1: OK 2: get 1: inc

↑ not atomic but
required by receptivity

NOT Receptive

What is a concurrent spec?

A spec (strategy) is a set σ
of "well-formed" traces s.t.

- non-empty
- prefix-closed

→ type-dependent
+ interleavings of
well-formed
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Lock Concurrent Spec

Lock \triangleq { acq : unit , rel : unit }

States : $\wp(\tau) \times \text{Maybe } \tau \times \wp(\alpha)$

(Waiting, owner?, releasing) $\xrightarrow{\alpha:\text{acq}}$ (Waiting \uplus { α }, owner?, releasing)

(Waiting, None, releasing) $\xrightarrow{\alpha:\text{OK}^{\text{acq}}}$ (Waiting, Some α , releasing)

(Waiting, Some α , releasing) $\xrightarrow{\alpha.\text{rel}}$ (Waiting, None, releasing \uplus { α })

(Waiting, owner?, releasing \uplus { α }) $\xrightarrow{\alpha:\text{OK}^{\text{rel}}}$ (Waiting, owner?, releasing)

Conditional Spec : Assumes agents alternate acquire calls with release calls

Executions of Concurrent Implementations

Given a concurrent implementation

$$M[R] = (M[\alpha])_{\alpha \in R}$$

we define its set of executions as

$$\widehat{M[R]} \triangleq \parallel_{\alpha \in R} \widehat{M[\alpha]}$$

The identity

Identity for implementation composition: (sequential)

$id: F \rightarrow F$

$f(a_1, \dots, a_n) \{$

$v \leftarrow f(a_1, \dots, a_n);$

$\text{ret } v$

$\}$

The identity

Identity for implementation composition: (sequential)

$Id: T \rightarrow T$

$f(a_1, \dots, a_n) \{$

$v \leftarrow f(a_1, \dots, a_n);$

$\text{ret } v$

$\}$

Example:

```
inc() {  
  v ← inc();  
  ret v  
}
```

```
get() {  
  v ← get();  
  ret v  
}
```

$Id: T \text{Counter} \rightarrow \text{Counter}$

```
inc  
↳ inc → ok ↗  
  
get  
↳ get → v ↗
```

$\hat{Id}: T \text{Counter} \rightarrow T \text{Counter}$

```
get  
↳ get → 3 ↗  
    3 → inc  
    ↳ inc → ok ↗ ...
```

The identity

Identity for Implementation (sequential) composition:

$$Id: \tau F \rightarrow F$$

```

f(a1, ..., an) {
  v ← f(a1, ..., an);
  ret v
}
    
```

Concurrent Identity

$$Id[\tau] \triangleq (Id)_{\leftarrow \tau}$$

Example:

```

inc() {
  v ← inc();
  ret v
}

get() {
  v ← get();
  ret v
}
    
```

...

```

inc() {
  v ← inc();
  ret v
}

get() {
  v ← get();
  ret v
}
    
```

$$\widehat{Id}[\tau] : \tau \text{Counter} \rightarrow \tau \text{Counter}$$

$$\tau = \{1, 2\}$$

1: inc 2: get 2: v
 2: get 2: v 1: inc 1: OK

2: inc $\tau \text{Counter}$
 \uparrow
 $\tau \text{Counter}$

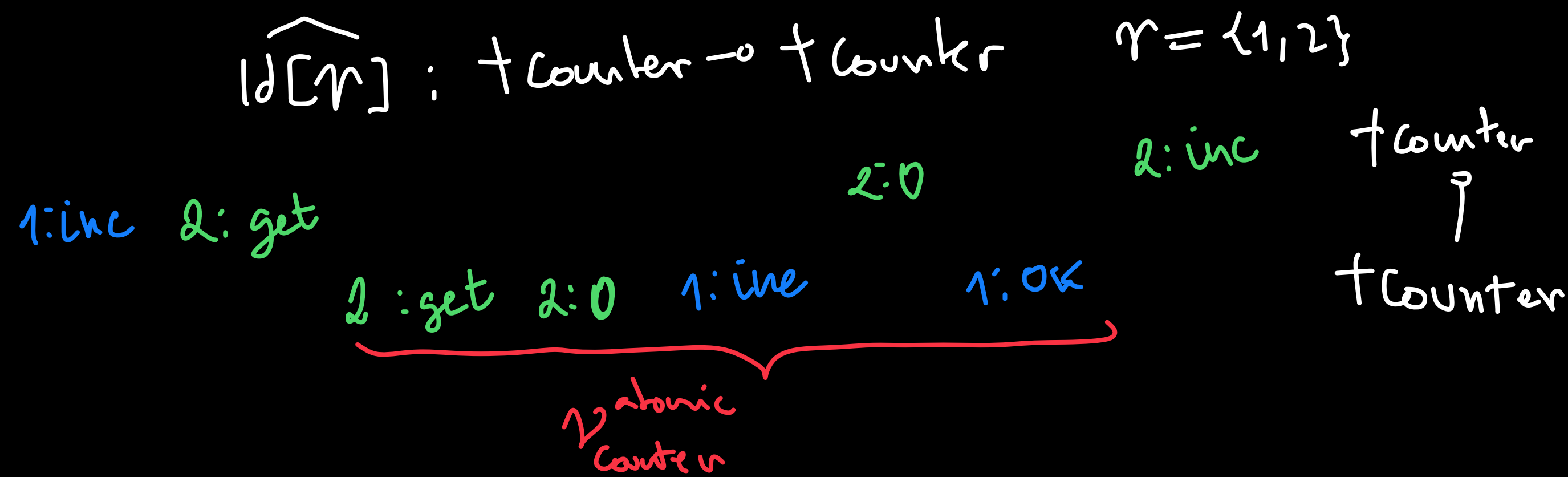
Spec + Implementation Composition

$$\left. \begin{array}{l} \mathcal{V}_E : \tau_E \\ \widehat{M}[\gamma] : \tau_E \rightarrow \tau_F \end{array} \right\} \mathcal{V}_E; \widehat{M}[\gamma] : \tau_F$$

$$\mathcal{V}_E; \widehat{M}[\gamma] = \{ t \in \tau_F \mid t \in \widehat{M}[\gamma] \wedge t \upharpoonright_E \in \mathcal{V}_E \}$$

↳ projection of t to only F events

Example:



Spec + Implementation Composition

$$\gamma_E : T_E$$

$$\widehat{M}[\gamma] : T_E \rightarrow T_F$$

$$\left. \begin{array}{l} \gamma_E : T_E \\ \widehat{M}[\gamma] : T_E \rightarrow T_F \end{array} \right\} \gamma_E; \widehat{M}[\gamma] : T_F$$

$$\gamma_E; \widehat{M}[\gamma] = \{ t \in T_F \mid t \in \widehat{M}[\gamma] \wedge t \uparrow_E \in \gamma_E \}$$

↳ projection of t to only F events

Example:

$$\widehat{Id}[\gamma] : T_{Counter} \rightarrow T_{Counter} \quad \gamma = \{1, 2\}$$

1: inc 2: get

2: 0

2: inc \uparrow TCounter

2: get 2: 0 1: inc 1: OK

TCounter

↳ atomic counter

Side bar:

$$(N[\alpha])_{\alpha \uparrow \gamma} \circ (M[\alpha])_{\alpha \uparrow \gamma}$$

=

$$\widehat{M}[\gamma]; \widehat{N}[\gamma]$$

A Problem: Identity?

2: get 2: 0 1: inc 1: OK \in $\gamma_{\text{Counter}}^{\text{atomic}}$

\wedge

1: inc 2: get 2: 0 1: inc 1: OK \in $\text{Id}[r]$
2: inc

\Downarrow

1: inc 2: get 2: 0 2: inc $\in \gamma_{\text{Counter}}^{\text{atomic}} ; \text{Id}[r]$

but

1: inc 2: get 2: 0 2: inc $\notin \gamma_{\text{Counter}}^{\text{atomic}}$

A Problem: Identity?

$\text{Id}[r]$ is NOT the identity

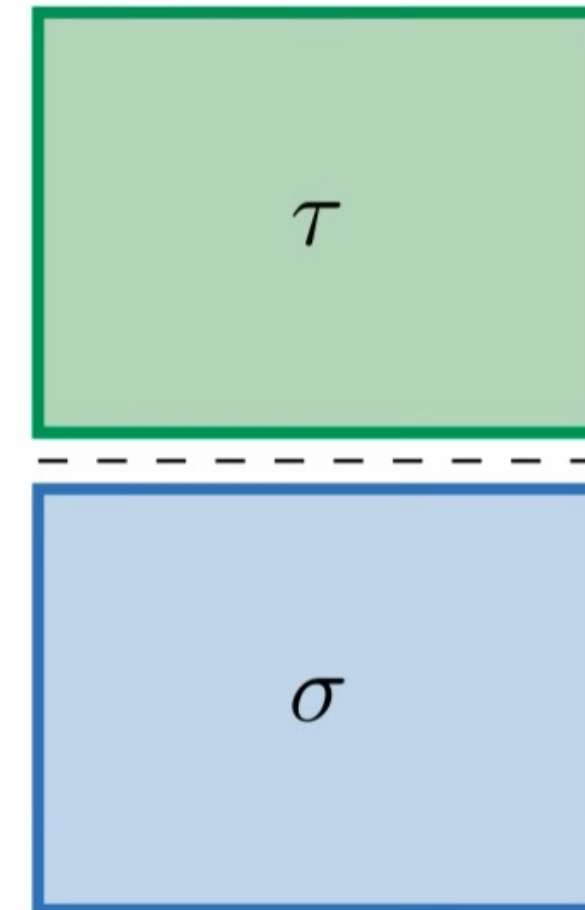
Algebra of Composition

There is a composition operation denoted by

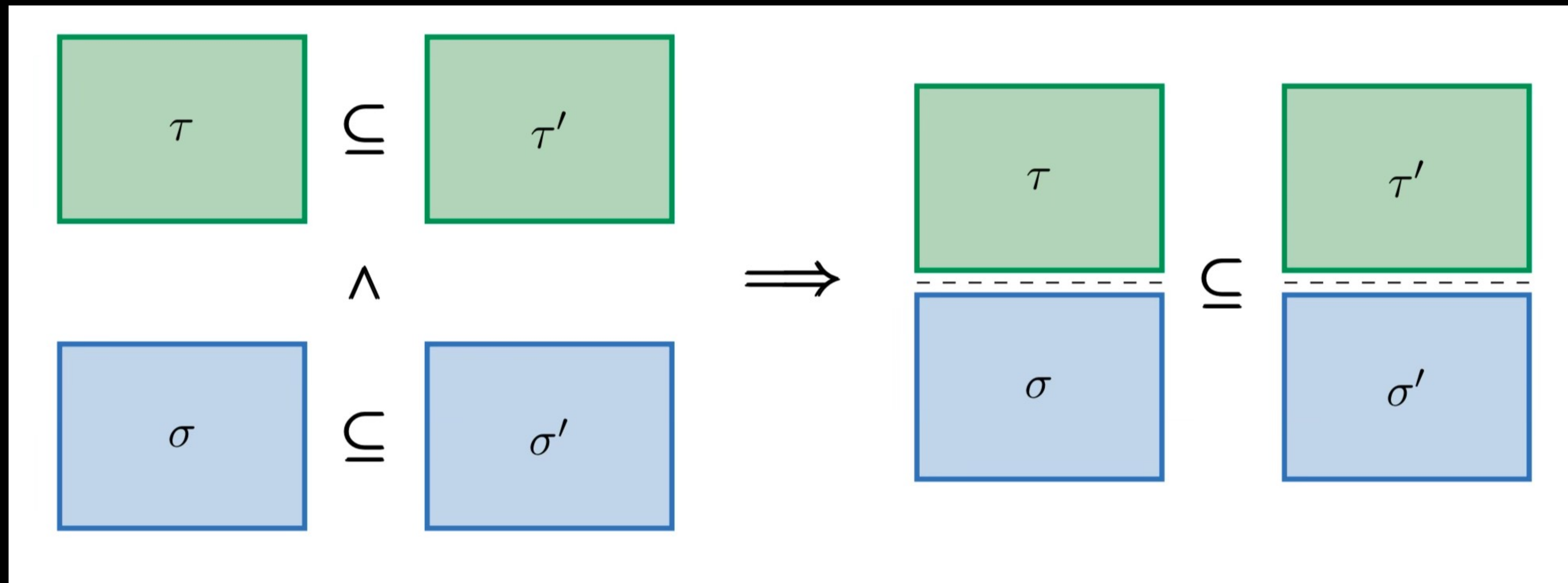
$$\sigma : A \multimap B \quad \tau : B \multimap C \longmapsto \sigma ; \tau : A \multimap C$$

Which is **associative** ... but there is **no identity element!**

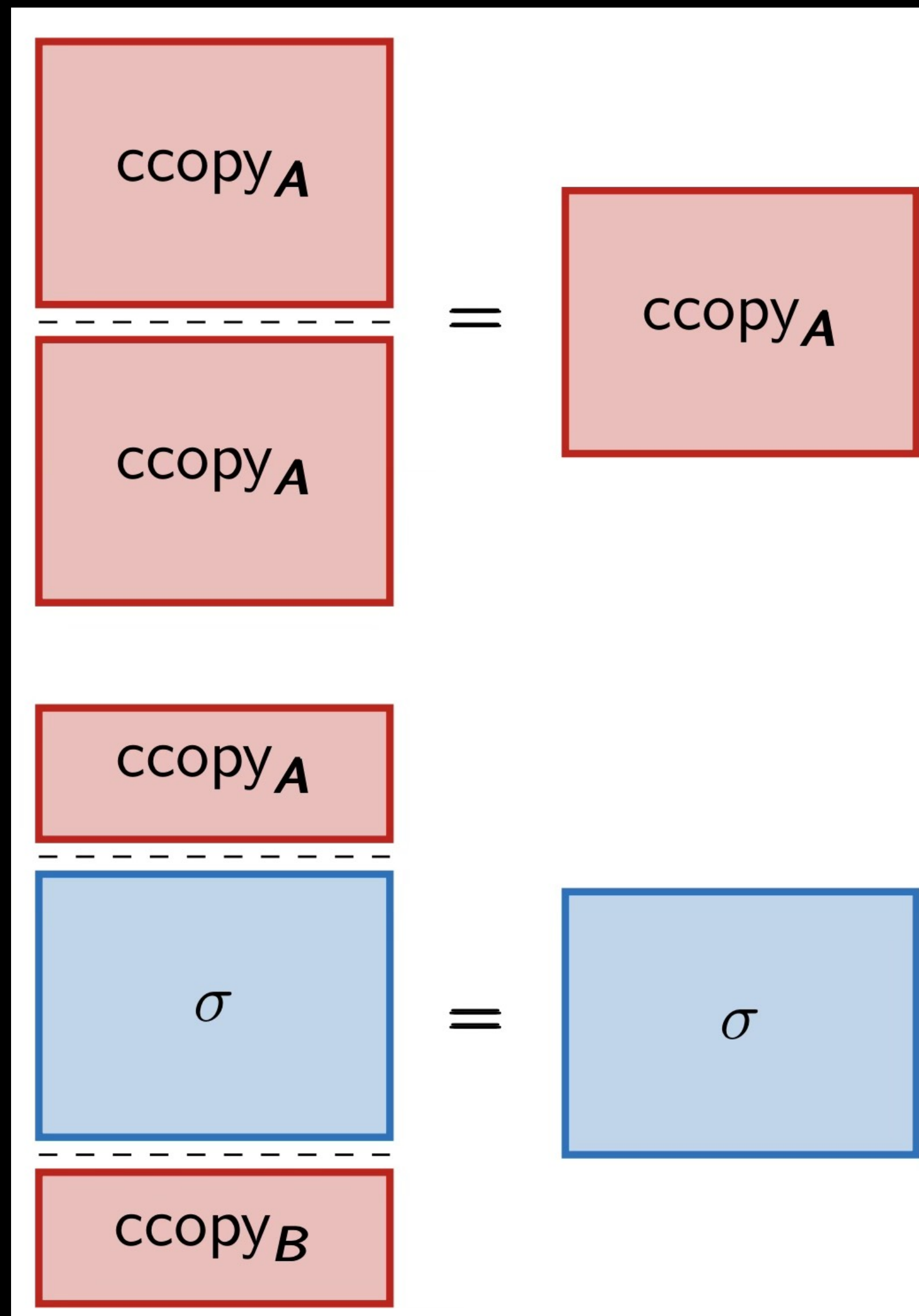
$$\forall \sigma : A \multimap B. \text{id}_A ; \sigma ; \text{id}_B = \sigma$$



Algebra of Composition



Algebra of Composition



$$\text{ccopy}_{+E} = \text{Id}[Y]: +E \rightarrow +E$$

→ idempotency

when $\sigma = \parallel_{\alpha \in Y} \widehat{M}[\alpha]$

Observational Refinement

Suppose

Concrete
Object



\supseteq



Abstract
Spec

Then



\supseteq



$=$



Linearizability

γ_E' is linearizable to γ_E
when

$$\boxed{\gamma_E' : \tau E} \subseteq \begin{array}{|l} \boxed{ID[\tau]} \\ \boxed{\gamma_E : \tau E} \end{array}$$

Notation

$$\gamma_E' \rightsquigarrow \gamma_E$$

Certified Concurrent Layers

A layer interface is pair

$$\mathcal{V}_E^1 \rightsquigarrow \mathcal{V}_E$$

A certified concurrent layer consists of

overlay spec ↗

$$\mathcal{V}_F^1 \rightsquigarrow \mathcal{V}_F$$

concurrent implementation ↗

$$M[\gamma]$$

underlay spec ↖

$$\mathcal{V}_E^1 \rightsquigarrow \mathcal{V}_E$$

$$\mathcal{V}_F^1 \subseteq \mathcal{V}_E^1; \widehat{M}[\gamma]$$

s.t. \wedge

$$\mathcal{V}_E^1; \widehat{M}[\gamma] \rightsquigarrow \mathcal{V}_F$$

Linearizability

\mathcal{V}_E' is linearizable to \mathcal{V}_E
when

$$\boxed{\mathcal{V}_E' : \tau E} \subseteq \begin{array}{|c|} \hline \text{Id}[\tau] \\ \hline \mathcal{V}_E : \tau E \\ \hline \end{array}$$

Notation

$$\mathcal{V}_E' \rightsquigarrow \mathcal{V}_E$$

When does this hold?

i.e. when does $t \in \mathcal{V}_E; \widehat{\text{Id}[\tau]}$?

Examples: Consider γ atomic counter as the abstract specification

Examples: Consider δ atomic counter as the abstract specification

1: inc 2: inc

1: inc 1: OK 2: inc 2: OK

1: OK 2: OK

→ more

concurrent

→ less

concurrent

allow
to
decrease
concurrency

Examples: Consider ^{atomic} counter as the abstract specification

1:inc 2:inc

1:inc 1:OK 2:inc 2:OK

1:OK 2:OK

→ more

→ less

concurrent
concurrent

allowed
to
decrease
concurrency

2: get

2: get

1: inc

2: 0

2: 0

→

1: inc

→

1: inc

appears
removed

allowed to
remove pending
invocations

Examples: Consider ^{atomic} counter as the abstract specification

1:inc 2:inc → 1:OK 2:OK → more concurrent allowed to decrease concurrency
→ less concurrent

2: get 1:inc 2:0 → 1:inc appears removed
2: get 2:0 → 1:inc removed
allowed to remove pending invocations

1:inc 2: get 2:1 → pending 1:inc
1:inc 1:OK 2: get 2:1 → completed 1:inc
allowed to complete pending invocations

Examples: Consider ^{atomic} counter as the abstract specification

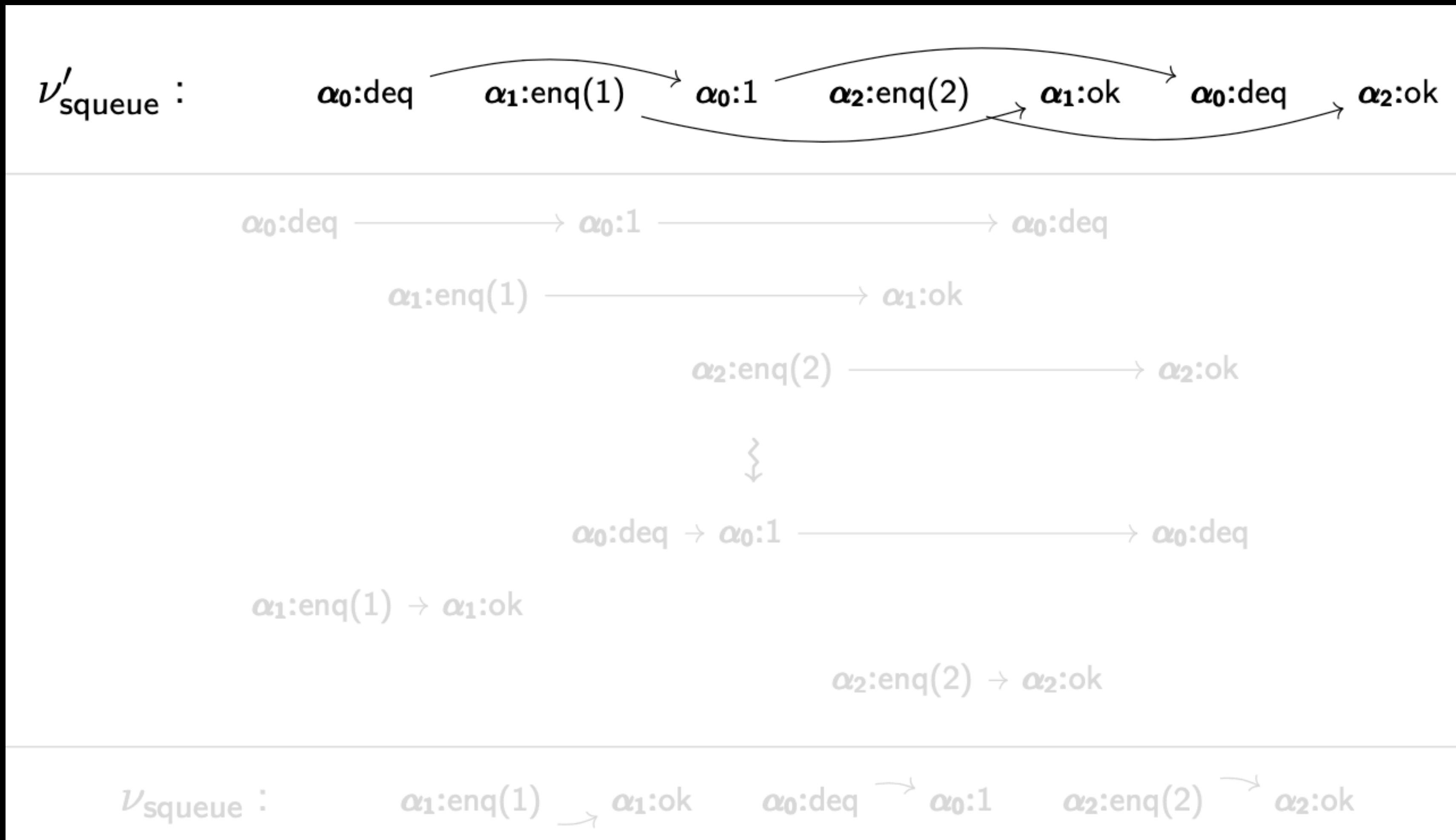
1:inc 2:inc → 1:OK 2:OK → more concurrent allowed to decrease concurrency
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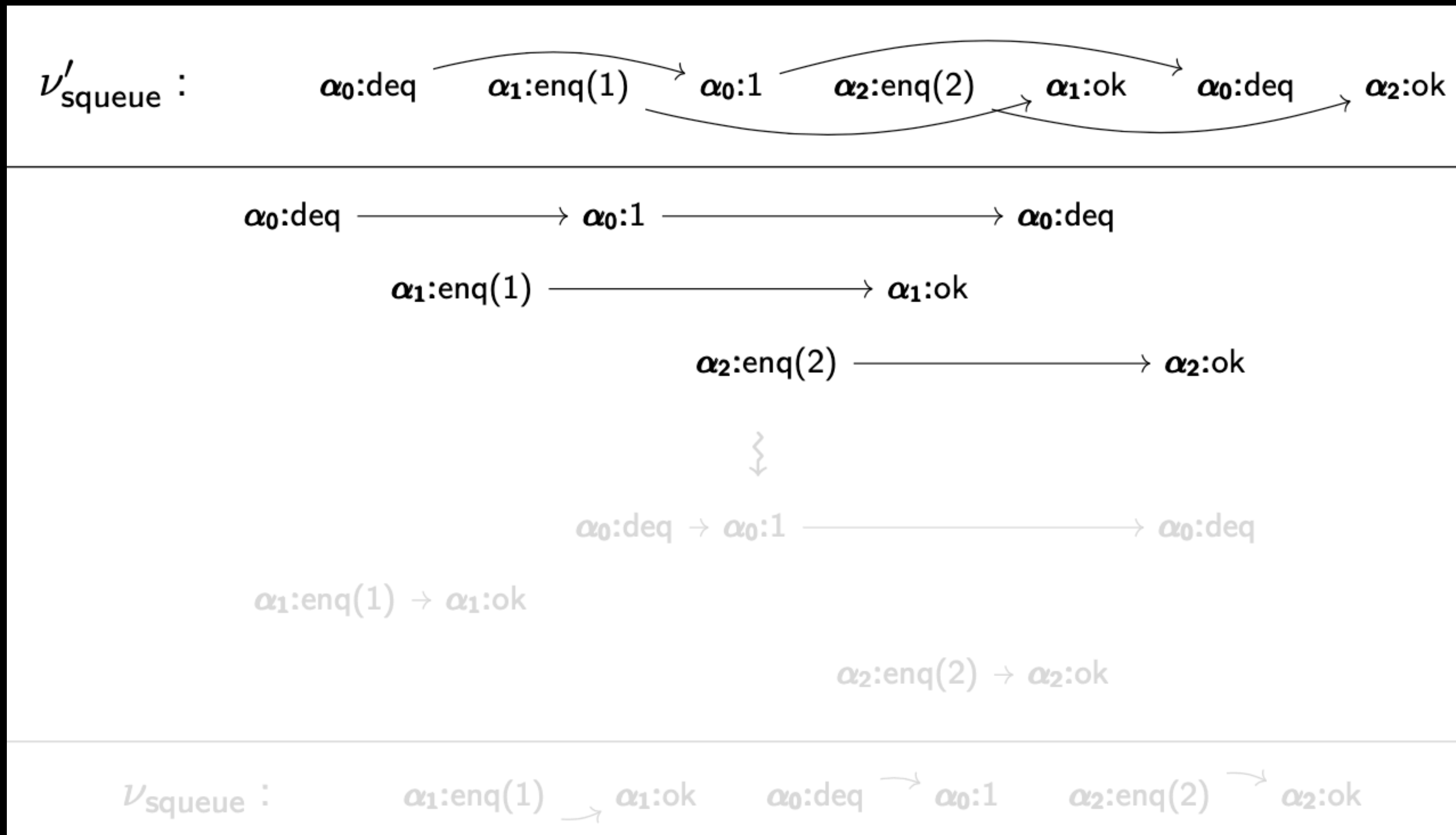
1:inc 2: get 2:1 → pending 1:inc
1:inc 1:OK 2: get 2:1 → completed 1:inc
allowed to complete pending invocations

1:inc 2: get 1:OK 2:0 2:inc 2:OK
2: get 2:0 1:inc 1:OK 2:inc 2:OK
CANNOT reorder events that happen before each other

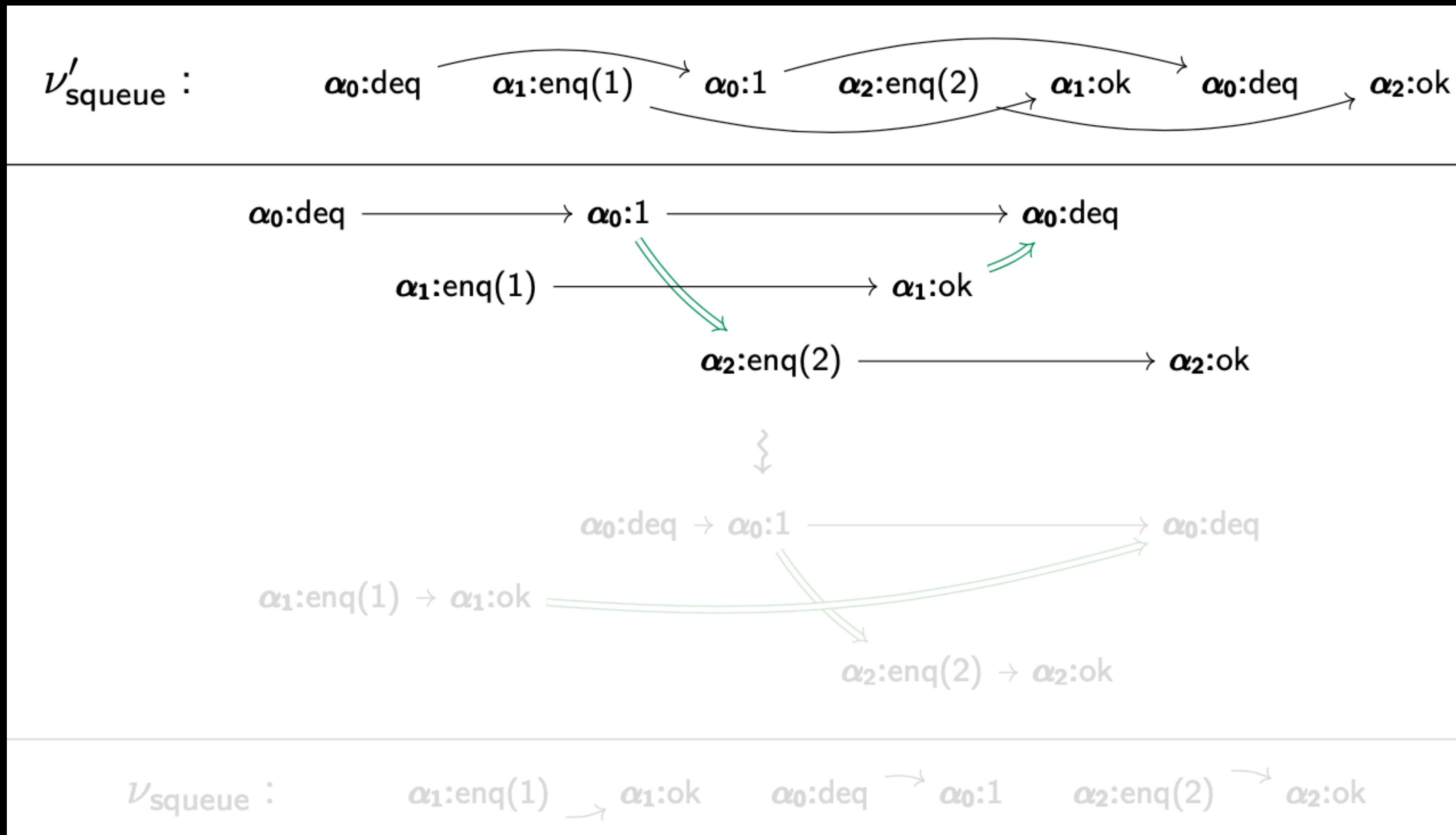
Another Example:



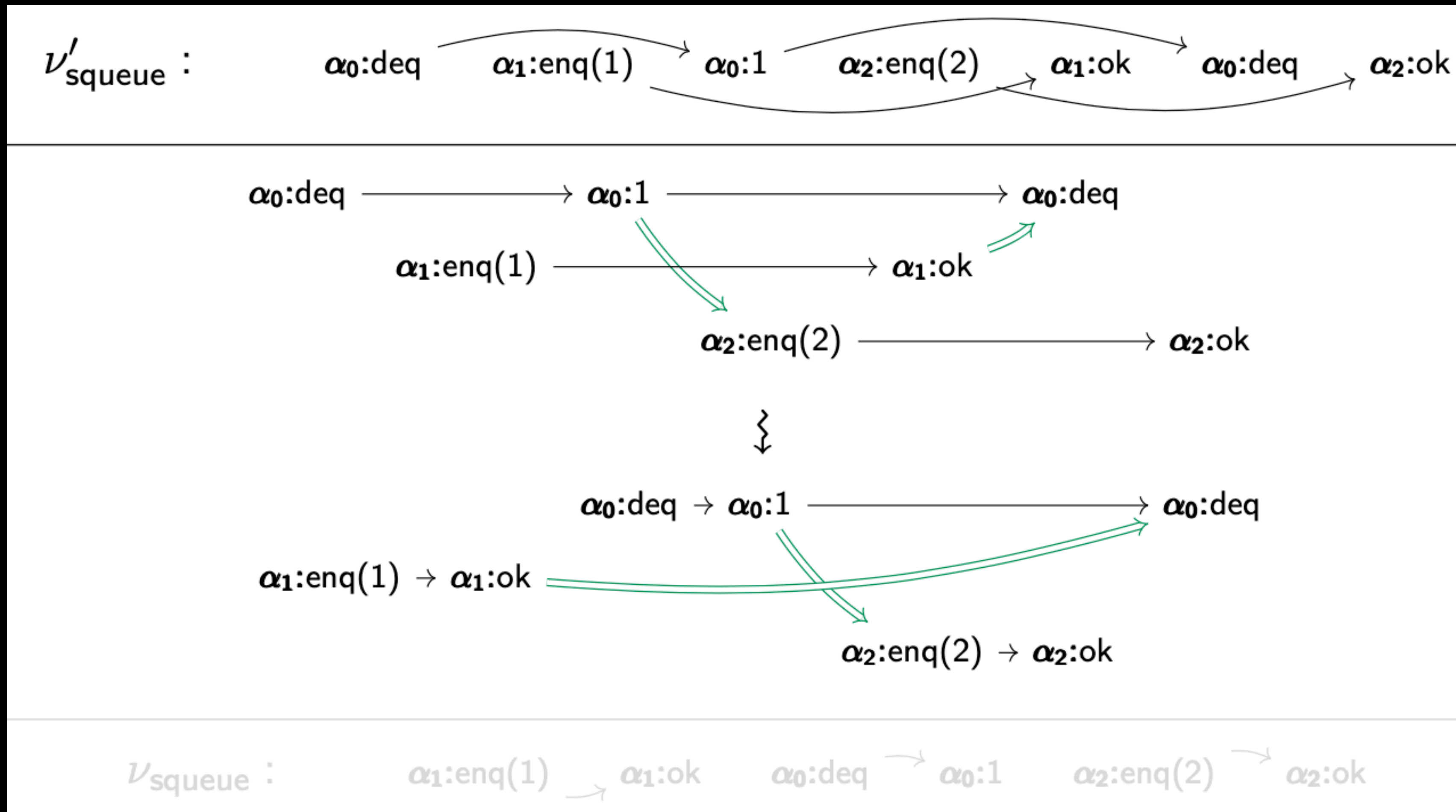
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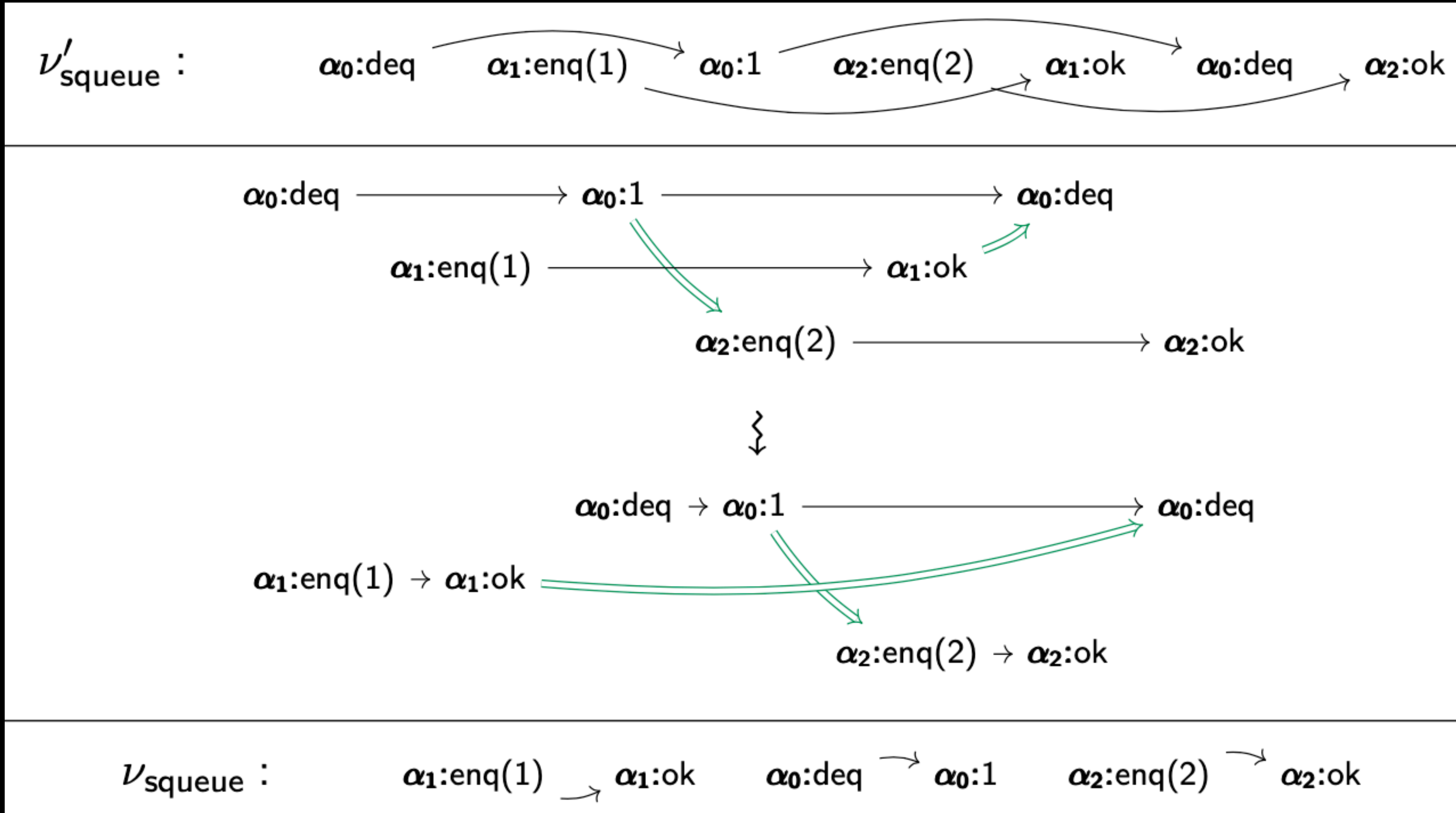
Another Example:



Another Example:



Another Example:



Concrete Linearizability

DEFINITION

s is linearizable to t when there exists a sequence s_O of invocations and a sequence s_P of responses such that

$$s \cdot s_P \rightsquigarrow_A t \cdot s_O$$

- ▶ t need not be atomic (coincides with Herlihy-Wing when it is);
- ▶ $s_P =$ returns;
- ▶ $s_O =$ removed pending invocations (not all need be removed);
- ▶ $\rightsquigarrow_A =$ happens-before order preservation.

$t' \in \mathcal{V}_E; \widehat{ID[V]} \iff \exists t \in \mathcal{V}_E. t' \text{ is linearizable w.r.t. } \mathcal{V}_E$

The Trap of Atomicity

Not every object is atomic

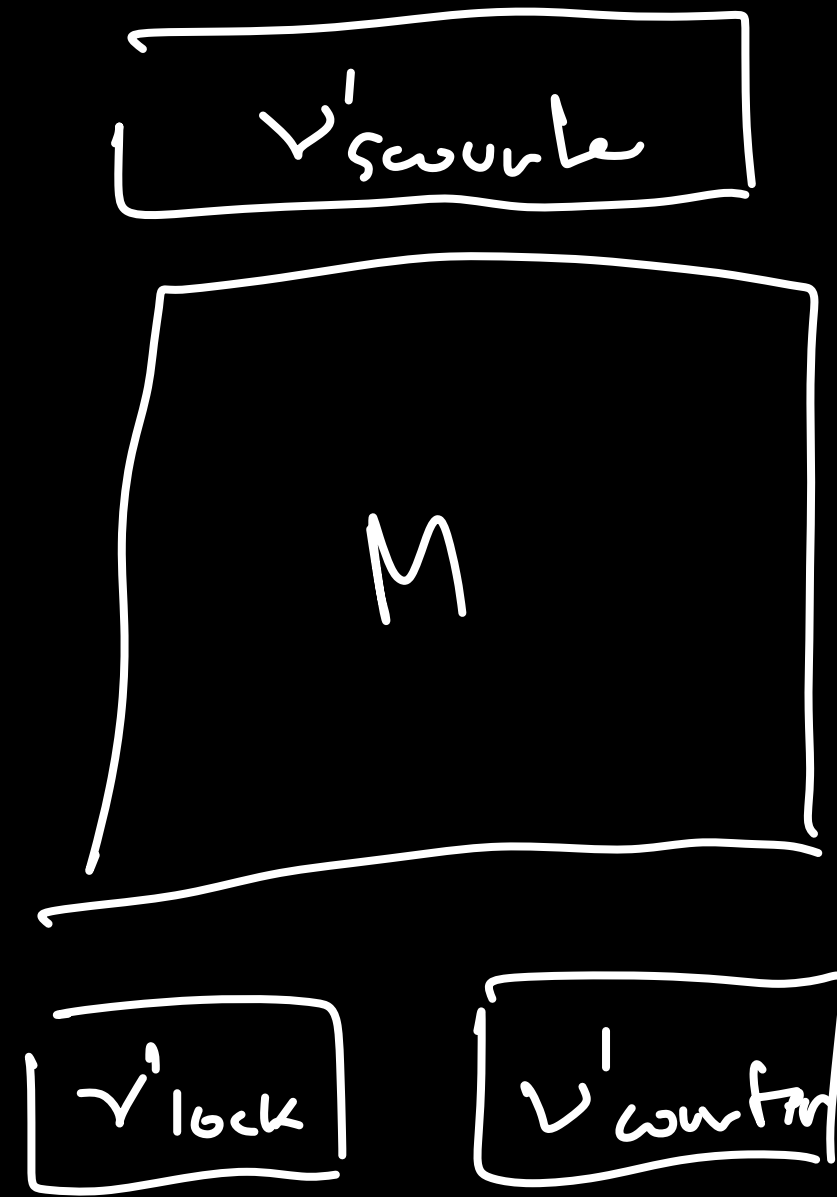
1: $\text{exch}(x)$ 2: $\text{exch}(y)$ 1: y 2: x

linearizes to

1: $\text{exch}(x)$ 1: y 2: $\text{exch}(y)$ 2: x

2: $\text{exch}(y)$ 2: x 1: $\text{exch}(x)$ 1: y

The Trap of Atomicity

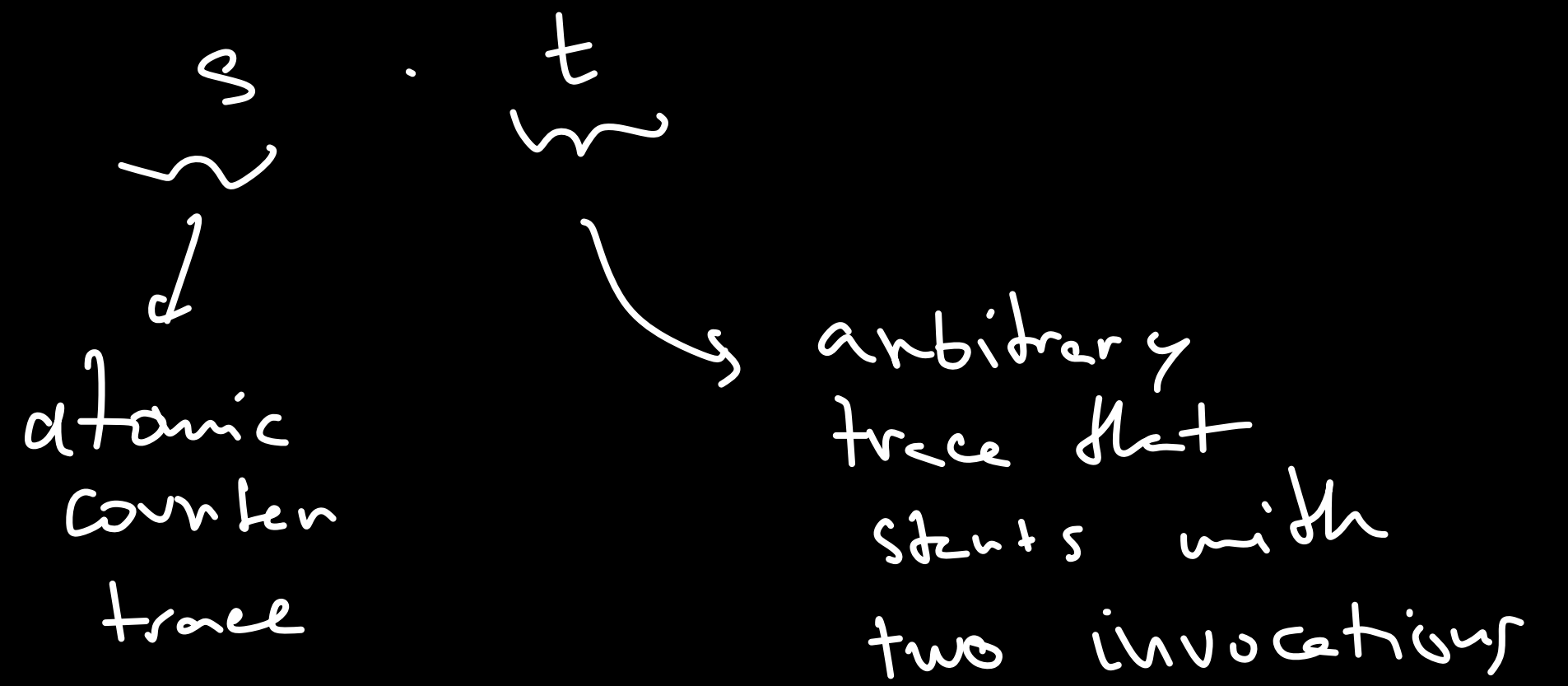


\leadsto atomic counter

```
inc(x) {
  - ← acq();
  v ← inc(x);
  - ← rel();
  ret v
}
```

\leadsto racy counter

every trace is



```
inc() {
  v ← x.rd();
  - ← x.wrt(v+1);
  ret OK
}
```

\leadsto lock \leadsto atomic lock \leadsto counter \leadsto racy counter